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a second voltage detector constructed of n-type components and responsive to the external voltage for producing a second signal indicative of the external voltage being greater than said predetermined value; and

a logic circuit responsive to said first and second signals for producing said first output signal.

474. The device of claim 472, wherein said second circuit portion includes:

a logic circuit responsive to said first output signal and the feedback signal for producing an output signal; and

a latch responsive to said output signal of said logic circuit for producing said first enable signal.

475. The device of claim 472, additionally comprising a reset circuit interposed between said first and second circuit portions for receiving said first output signal from said first circuit portion and for terminating said first output signal when predetermined stability requirements are not meet.

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

<b>Applicant(s):</b> Keeth, et al.	)	
	)	<b>Examiner:</b> Not yet assigned
<b>Serial No.:</b> 09/893,389	)	
	)	<b>Art Unit:</b> 2818
<b>Filed:</b> 28 June 2001	)	
<b>Entitled:</b> 256 MEG DYNAMIC RANDOM ACCESS MEMORY		

**Complete Clean Set Of Claims Currently Pending**

443. A device responsive to first and second external signals for controlling a power up of a first voltage supply, comprising:

a first circuit responsive to the first external signal for producing a first output signal indicative of whether the first external signal satisfies a predetermined condition; and

a second circuit responsive to the first output signal and the second external signal for producing a first enable signal to enable the first voltage supply.

444. The device of claim 443, wherein said first output signal is indicative of the first external signal being greater than a first predetermined voltage.

445. The device of claim 444, wherein said first predetermined voltage is approximately two volts.

446. The device of claim 444, wherein said first circuit includes:

a first voltage detector responsive to the first external signal for producing a first signal indicative of the first external signal being greater than said first predetermined voltage;

a second voltage detector responsive to the first external signal for producing a second signal indicative of the first external signal being greater than said first predetermined voltage; and